INTERNATIONAL JOURNAL OF EMBEDDED AND REAL-TIME COMMUNICATION SYSTEMS

January-March 2013, Vol. 4, No. 1

Table of Contents

Special Theme Issue on System-on-Chip

GUEST EDITORIAL PREFACE

Jari Nurmi, Department of Electronics and Communications Engineering, Tampere University of Technology, Tampere, Finland

Research Articles

Instrumentation-Driven Model Detection and Actor Partitioning for Dataflow Graphs

Institute for Advanced Computer Studies, University of Maryland, College Park, MD, USA
Shuoxin Lin, Department of Electrical and Computer Engineering, Institute for Advanced Computer Studies, University of Maryland, College Park, MD, USA
William Plishker, Department of Electrical and Computer Engineering, Institute for Advanced Computer Studies, University of Maryland, College Park, MD, USA
William Plishker, Department of Electrical and Computer Engineering, Institute for Advanced Computer Studies, University of Maryland, College Park, MD, USA
Chung-Ching Shen, Department of Electrical and Computer Engineering, Institute for Advanced Computer Studies, University of Maryland, College Park, MD, USA
Shurar S. Bhattacharuna Department of Electrical and Computer Engineering Institute for Advanced Computer Studies

Shuvra S. Bhattacharyya, Department of Electrical and Computer Engineering, Institute for Advanced Computer Studies, University of Maryland, College Park, MD, USA

A Simulation-Based Framework for the Exploration of Mapping Solutions on Heterogeneous MPSoCs Antonio Miele, Politecnico di Milano, Milan, Italy Christian Pilato, Politecnico di Milano, Milan, Italy Donatella Sciuto, Politecnico di Milano, Milan, Italy

Design Flow for Silicon Chip Implementing Novel Platform Architecture for Wireless Communication Prabhat Avasare, IMEC, Heverlee, Belgium Jeroen Declerck, Bird and Goen, Leuven, Belgium

Jeroen Deciercs, Bra and soen, Leaven, Belgium Miguel Glassee, IMEC, Heverlee, Belgium Amir Amin, IMEC, Heverlee, Belgium Erik Umans, IMEC, Heverlee, Belgium Praveen Raghavan, IMEC, Heverlee, Belgium Martin Palkovic, IT4Innovations, Ostrava, Czech Republic

Efficient Implementation of Application-Aware Spinlock Control in MPSoCs Diandian Zhang, RWTH Aachen University, Aachen, Germany Li Lu, RWTH Aachen University, Aachen, Germany Jeronimo Castrillon, RWTH Aachen University, Aachen, Germany Torsten Kenpf, RWTH Aachen University, Aachen, Germany Gerd Ascheid, RWTH Aachen University, Aachen, Germany Rainer Leupers, RWTH Aachen University, Aachen, Germany Bart Vanthournout, Synopsys Inc., Leuven, Belgium

EVENT REPORT

Event Report: International Symposium on System-on-Chip 2012 Jari Nurmî, Department of Electronics and Communications Engineering, Tampere University of Technology, Tampere, Finland

Copyright

The International Journal of Embedded and Real-Time Communication Systems (ISSN 1947-3176; eISSN 1947-3184). Copyright © 2013 IGI Global. All rights, including translation into other languages reserved by the publisher. No part of this journal may be reproduced or used in any form or by any means without written permission from the publisher, except for noncommercial, educational use including classroom teaching purposes. Product or company names used in this journal are for identification purposes only. Inclusion of the names of the products or companies does not indicate a claim of ownership by IGI Global of the trademark or registered trademark. The views expressed in this journal are those of the authors but not necessarily of IGI Global.

The International Journal of Embedded and Real-Time Communication Systems is currently listed or indexed in: ACM Digital Library, Bacon's Media Directory; Cabell's Directories; DBLP; Google Scholar; INSPEC; JournalTOCs; Library & Information Science Abstracts (LISA); MediaFinder; SCOPUS; The Standard Periodical Directory; Ulrich's Periodicals Directory